

Sc 1023/TG

JC474 U.S. PRO  
09/683243

		Subclass
	Class	

**PATENT NUMBER**

Best Available Copy  
U.S. UTILITY Patent Application

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

SCANNED: M3 6.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/683243	D	326 713	300	2819	220

## APPLICANTS

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## Integrated level shift and override circuit for a Processor system

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE		
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER		
	(Assistant Examiner)	(Date)	(Primary Examiner)	(Date)	(Legal Instruments Examiner)	(Date)

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